# Design, Development And Implementation Of A Smart And Economically Viable Solar Power Charge Controller.

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**ABSTRACT:-** A Smart and economically viable solar power charge controller has been design and implemented. The system was implemented using Arduino Microcontroller. The microcontroller was used in order to make the system smart, robust and simple. The system was tested under different atmospheric conditions. The result obtained shows that the developed charge controller performs efficiently. The system is also shown to be economically viable as the major component in its development is the Arduino microcontroller.

## I. INTRODUCTION.

Electricity is one of the most essential needs for humans in the present. Conversion of solar energy into electricity does not only improve generation of electricity but also reduces pollution from fossil fuels. The output power of solar panel depends on solar irradiance, temperature and the load impedance. As the load impedance is dependent on application, a dc-dc converter is used for improving the performance of solar panel [12].

Solar PV is one of the renewable energy sources used in present days technology. PV has many benefits especially in environmental, economic and social aspects. In general, a PV system consists of a PV array which converts sunlight to direct-current electricity, a control system which regulates battery charging, and the load [12]

A charge controller is one of functional and reliable major components in PV systems. A good, solid and reliable PV charge controller is a key component of any PV battery charging. The main function of a charge controller in a PV system is to regulate the voltage and current from PV solar panels into a rechargeable battery. Solar charge controllers play an important part in isolated solar systems. The minimum function of a solar charge controller is to disconnect the array when the battery is fully charged and keep the battery fully charged without damage. This goal is to ensure the batteries are working in optimal conditions, mainly to prevent overcharging (i.e. by disconnecting solar panel when batteries are full) and too deep discharge (by disconnecting the load when necessary). It also offers protection against the reverse current flow at night back to the PV [14]

This research was implemented to overcome the challenges of previous works. It has the advantage of adding a microcontroller (ATMEGA328P-PU) chip, liquid crystal display (LCD) for readability of the voltage across the solar panel and, also the battery, this work operates during day and/ or night, it has the advantage of connecting DC load on the system, simple and economically viable.

## II. DESIGN AND IMPLEMENTATION

In this section, we present the design of the solar charge controller. By knowing design specification and available input parameters, we can have an accurate and simple relationship between the input and output to perform the require task.

## III. DESIGN PROCEDURES

The developed charge controller consists the following units:

- 1. Voltage sensing
- 2. PWM signal generation
- 3. MOSFET switching and driver
- 4. Filter and protection
- 5. Display and Indication
- 6. Software design

# IV. VOLTAGE SENSORS

The main sensor in the charge controller is voltage sensors which can easily be implemented by using a voltage divider circuit. We have to sense voltage coming from solar panel and the battery voltage.

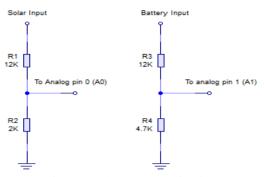


Figure 1 voltage sensor circuit

The charge controller was designed for a 6V, 5w battery and, 16V solar panel. Higher voltage values can be implemented by changing the divider resistors values.

# Software code

ADC Calibration : actual volt/divider output= 3.127 2.43 V is eqv to 520 ADC 1 is eqv to .004673V

### **ARDUINO CODE:**

for(int i=0;i<150;i++)
{ sample1+=analogRead(A0); //read the input voltage from solar panel
sample2+=analogRead(A1); // read the battery voltage
delay(2);
}
sample1=sample1/150;
sample2=sample2/150;
solar\_volt=(sample1\*4.673\* 3.127)/1000;
bat\_volt=(sample2\*4.673\* 3.127)/1000;</pre>

## V. PWM SIGNAL GENERATION

PWM (pulse width modulation) is a technique that is simply used to control a digital output signal by switching it on and off very quickly, by varying the width of the on/off duration, it will give the effect of varying the output voltage [11].

$$Vout = \frac{Ton}{(Ton + Toff)} \times Vin$$

Time period (T) = Ton + Toff So Vout =  $\frac{Ton}{T} \times Vin$ Where  $\frac{Ton}{(Ton + Toff)} \times 100$  or  $\frac{Ton}{T} \times 100$  is called Duty Cycle

# VI. MOSFET SWITCHING AND DRIVER

In this charge controller two MOSFETs where used in which one is for controlling the power flow from solar panel to battery and other is to drive the load. A gate driver is a power amplifier that accepts a lowpower input from a micro controller and produces a high-current drive input for the gate of a high-power MOSFET.

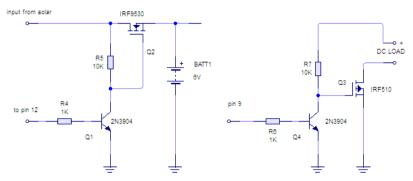


Figure 2: MOSFET driver circuit

This circuit is really important in this PV charge controller in order to amplify and translate the PWM signal from Arduinor microcontroller (ATMEGA328) to trigger POWER MOSFET (IRF9530 and IRF540 this MOSFET can withstand up to 100v) gate and the high output.

#### **Calculation of components values**

A resistor  $R_B$  is connected to the base of the transistor to limit the maximum current through the base circuit to 4mA, and the value of  $R_B$  was obtained as follows;

 $V_{BB} - I_B R_B - V_{BE} = 0$ 

 $V_{BB} = +5V$ , (maximum input signal from microcontroller),  $V_{BE} = 0.7V$ 

 $5 - (4 \times 10^{-3} \times R_B) - 0.7 = 0$ 

 $0.004 R_{\rm B} = 5 - 0.7 = 4.3$  $R_{\rm B} = \frac{4.3}{0.004} = 1070 \approx 1.07 \text{ K}\Omega$ 

 $R_{\rm B} = 1 K \Omega$  was used

In order to limit the maximum collector current  $(I_c)$  to 1.2mA when the output of the transistor is low, a pull up resistor is connected between +12V supply.

$$\begin{split} V_{CC} &- I_C R_C - V_{CE} = 0 \\ 12 &- I_C R_C - 0 = 0 \ (V_{CE} = 0 \ at \ saturation) \\ 12 &- (1.2 \times 10^{-3} \times R_c) - 0 = 0 \\ 12 &- (0.0012 \ R_c) = 0 \\ 0.0012 \ R_c = 12 \\ R_c &= \frac{12}{0.0012} = 10000 = 10 K \Omega \end{split}$$

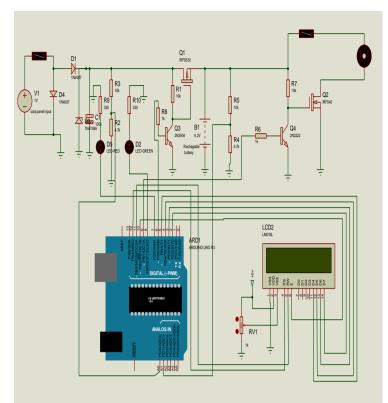
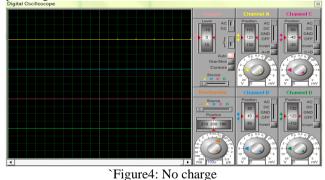


Figure3: Proteus Software Circuit Implementation

Oscilloscope was used to get the PWM output from the microcontroller. The PB5SACK pin generates square waves. Therefore, the 12th pin of the microcontroller was connected to the Channel A of the oscilloscope. The output of three different voltage of the battery cell is given thus.

### Not Charging:

In the figure below it shows that the duty cycle of PWM is zero (0%), which mean the battery is not charging from the solar panel this is because the voltage of the battery is below 6.0vor the voltage drown from the solar panel is less than the battery voltage.



Float Charge: \*

In this case the charging of the system is increasing between 6.2V to 7.0V with duty cycle of 10% to 89%. Below diagram shows PWM of 10% duty cycle

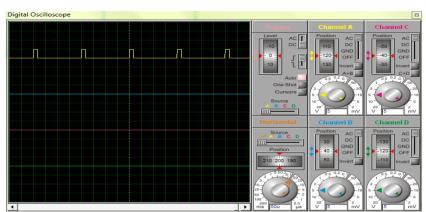


Figure 5 10% pulse width Bulk Charge: It is when voltage is between 7.1 V to 7.4Vof it fully charged. Duty cycle used: 90 %

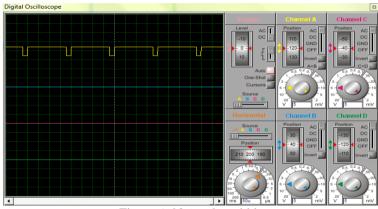


Figure 6 90% pulse width

# TESTING

After the completion of the project, various test and measurements were carried out on the circuit to ascertain its workability. These include continuity test, short circuit and open circuit tests, dc voltage level tests; these were done using a digital multimeter.

The assurance of project work was determined by testing, which makes it necessary to test the circuit during and after construction. The main three test where carried out during the construction. These tests are stated below.

- i. Temporary test
- ii. Permanent test
- iii. Final Test after casing

# **Temporary Test**

This is a type of test that is done by assembling the components on the testing board (braed-board) after software simulation to testify its working practically normal. Below is the typical picture of the assembled component in the bread board.

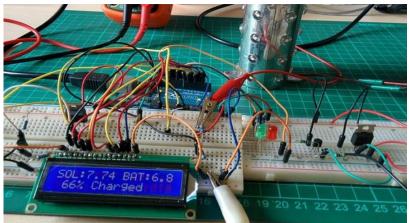


Figure.7 Diagram of Temporal Construction

# **Permanent Test**

This is a kind of test that takes place after construction that is after fixing the components in the pin solder terminal of the permanent board (vero board). The short circuit test, open circuit test and continuity test were carried out, so as to make sure the proper working of the circuit before the casing of the entire system, as it's pictorially shown below.

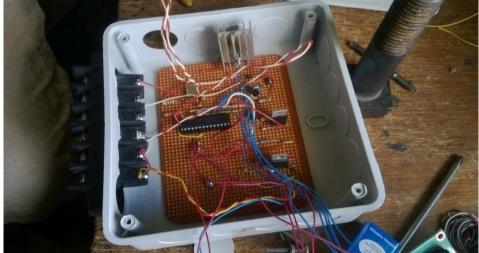


Figure8 Diagram of Permanent Construction

# **Final Test after Casing**

The time taken to recharge any battery is dependent on the voltage and current applied to it. If the recharged current or voltage is too low, then the recharge time will be relatively long; if the current or voltage is high then the recharge time will be short. After construction and casing of project, the readings were taken using 12V/5W solar panel and 6V/4.5h lead-acid rechargeable battery. This test was carried out and the result was tabulated below (4.3).

Table 4.1 Result Obtained						
s/n	Solar	Battery	PWM %	DC Load	Green LED	Read LED
1.	10.28V	4.1V	Not	ON	OFF	ON
			charging			
2.	10.26V	6.2V	16%	ON	Blinking	OFF
3.	10.26V	6.3V	25%	ON	Blinking	OFF
4.	10.38V	6.5V	41%	ON	Blinking	OFF
5.	15.48V	6.8V	66%	ON	Blinking	OFF
6.	15.13V	7.3V	83%	ON	Blinking	OFF
7.	13.49V	7.4V	91%	ON	ON	OFF

# VII. RESULTS

The table above show the result of the test carried out after the completion of the entire work. In the table above it can be observed that the voltages supply from the solar panel increases from the first to the final reading, this is because of the intensity of the sun ray which is not constant. the percentage of the respective reading of the voltage drawn from the solar and battery level of charging voltages were indicated and the green LED is

blinking showing that the battery is charging continuously, but red LED glows only when the voltage drawn from the solar panel is too low to charge the battery or if the charging battery is bad. Below is the pictorial diagram of the complete system under testing.



Figure.9 Pictorial Diagram of Complete Constructed Solar Charge Controller During Test.

# VIII. DISCUSSION OF RESULTS

The reading taken from this test which is tabulated above was at random time, and It was observed that, the battery took much of time before it riches above 90% (i.e full charge mode) level and this is because of the current drown from the 5Wsolar panel is very small and the test was conducted in cloudy day when there is no maximum sun light.

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